

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant(s): OKUNO, Tomohisa et al.

COPY

Application No.:

Group:

Filed: November 13, 2001

Examiner:

For: VOLTAGE CONVERSION CIRCUIT AND SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE PROVIDED THEREWITH

L E T T E R

Assistant Commissioner for Patents  
Box Patent Application  
Washington, D.C. 20231

November 13, 2001  
2936-0141P

Sir:

Under the provisions of 35 USC 119 and 37 CFR 1.55(a), the applicant hereby claims the right of priority based on the following application(s):

<u>Country</u>	<u>Application No.</u>	<u>Filed</u>
JAPAN	2000-344809	11/13/00

A certified copy of the above-noted application(s) is(are) attached hereto.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

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By: \_\_\_\_\_

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